

## Specification

| Parameter | 2.5 V Specifications |  |  | 3.3 V Specifications |  |  | Units | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | Min | Typ | Max | Min | Typ | Max |  |  |
| Supply Voltage ( $\mathrm{V}_{\mathrm{DD}}$ ) | 2.375 | 2.50 | 2.625 | 2.97 | 3.30 | 3.63 | V |  |
| Output Frequency |  | 125.00 |  |  | 125.00 |  | MHz |  |
| Frequency Stability | - 50 |  | + 50 | - 50 |  | + 50 | ppm | Includes supply voltage and temperature variation ( -40 to $85^{\circ} \mathrm{C}$ ), reflow drift, and aging. |
| Supply Current |  | 95 |  |  | 100 |  | mA | No load |
| Enable/Disable Time |  |  | 1 |  |  | 1 | us | Guaranteed by design |
| Input LOW level |  |  | $0.3 \mathrm{~V}_{\mathrm{DD}}$ |  |  | $0.3 \mathrm{~V}_{\mathrm{DD}}$ | V | At OE pin |
| Input HIGH level | 0. $7 \mathrm{~V}_{\mathrm{DD}}$ |  |  | 0. $7 \mathrm{~V}_{\mathrm{DD}}$ |  |  | V | At OE pin |
| Output LOW level |  | 0.8 | VDD-1.8 |  | 1.5 | VDD-1.8 | V |  |
| Output HIGH level | VDD-1.0 | 1.6 |  | VDD-1.1 | 2.3 |  | V |  |
| Amplitude ( $\mathrm{V}_{\mathrm{A}}$ ) |  | 0.75 |  |  | 0.75 |  | V | Single Ended output swing (Pk-Pk) |
| Mid Level ( $\mathrm{V}_{\mathrm{M}}$ ) |  | VDD -1.3 |  |  | Vdd -1.3 |  | V |  |
| Rise Time ( $\mathrm{T}_{\mathrm{R}}$ ) |  | 200 | 240 |  | 200 | 240 | ps | Maximum; 20/80\% of $\mathrm{V}_{\mathrm{A}}$; Output load (CL) = 2 pF ; Guaranteed by Char. |
| Fall Time ( $\mathrm{T}_{\mathrm{F}}$ ) |  | 200 | 240 |  | 200 | 240 | ps | Maximum; 20/80\% of $\mathrm{V}_{\mathrm{A}}$; Output load (CL) = 2 pF ; Guaranteed by Char. |
| Symmetry (SYM) | 48 | 50 | 52 | 48 | 50 | 52 | \% | Worst case; measured at 50\% of waveform |
| Phase Jitter |  | 0.9 |  |  | 0.6 |  | ps | 12k to 20MHz, RMS; Measured Differentially |
| Period Jitter |  | 2.4 |  |  | 2.2 |  | ps | RMS |
| Cycle-to-Cycle Jitter |  | 18 |  |  | 16 |  | ps | 1,000 cycles, Peak |
| Start-up Time |  | 10 |  |  | 10 |  | ms | Output valid time after power up, $25^{\circ} \mathrm{C}$ |
| Aging |  | $\pm 5$ |  |  | $\pm 5$ |  | ppm | $25^{\circ} \mathrm{C}, 10$ years |

## Block Diagram



Output Waveform


## Part Ordering Information

| Package Size | Voltage | Ordering Code |
| :---: | :---: | :---: |
| $7.0 \times 5.0 \mathrm{~mm}$ | 3.3 V | 4MA125000Z3AACUGI |
|  | 2.5 V | 4MA125000Z3BACUGI |
| $5.0 \times 3.2 \mathrm{~mm}$ | 3.3 V | 4MA125000Z3AACTGI |
|  | 2.5 V | 4MA125000Z3BACTGI |
| * Factory minimum order quantity: $500 \mathrm{pcs}(\mathrm{T} / \mathrm{R})$ |  |  |



## Pin Description

| Pin \# | Name | Description |
| :---: | :---: | :---: |
| 1 | OE | Output Enable* |
| 2 | NC | No Connect |
| 3 | GND | Ground |
| 4 | OUT+ | Output |
| 5 | OUT- | Complementary Output |
| 6 | VDD | Power Supply Voltage |
| *Pulled high internally |  |  |

## Solder Reflow Profile



Package Outline and Dimensions
Typical PCB Land Pattern
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## ()IDT

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