TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH86F

2-Input EXCLUSIVE OR Gate

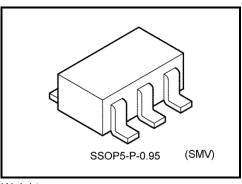
Features

High speed operation : t_{pd} = 4.8 ns (typ.) at V_{CC} = 5V, 15pF
 Low power dissipation : I_{CC} = 2µA (max) at Ta = 25°C
 High noise immunity : V_{NIH} = V_{NIL} = 28% V_{CC} (min)

• 5.5-V tolerant inputs.

• Balanced Propagation Delay : $t_{pLH} \approx t_{pHL}$

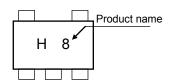
• Wide operating voltage range: V_{CC} = 2 to 5.5 V



Weight

SSOP5-P-0.95 : 0.016 g (typ.)

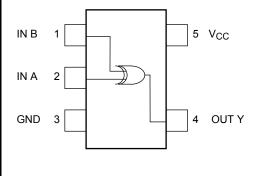
Marking



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	– 0.5 to 7	V
DC input voltage	V _{IN}	– 0.5 to 7	V
DC output voltage	V _{OUT}	-0.5 to $V_{CC}+0.5$	V
Input diode current	I _{IK}	- 20	mA
Output diode current	lok	± 20 (Note 1)	mA
DC output current	lout	± 25	mA
DC V _{CC} /ground current	Icc	± 50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	- 65 to 150	°C
Lead temperature (10s)	TL	260	°C

Pin Assignment (top view)



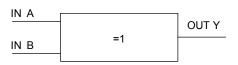
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

Start of commercial production 1994-06

IEC Logic Symbol



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	– 40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
	ui/uv	0 to 20 (V _{CC} = 5.0 V \pm 0.5 V)	115/ V

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Electrical Characteristics

DC Characteristics

Characteristics Symbol		T (0 III)			Ta = 25°C			Ta = -40 to 85°C		
		Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage		_		2.0	1.5	_	_	1.5	_	
				3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
Low lovel input				2.0		_	0.5	_	0.5	V
Low-level input voltage		_		3.0 to 5.5		_	V _{CC} × 0.3	_	$\begin{array}{c} V_{CC} \\ \times 0.3 \end{array}$	
			Ι _{ΟΗ} = -50 μΑ	2.0	1.9	2.0	_	1.9		. V
	V _{ОН}	V _{IN} = V _{IH} or V _{IL}		3.0	2.9	3.0	_	2.9		
High-level output voltage				4.5	4.4	4.5	_	4.4		
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80		
Low-level output voltage		V _{IN} = V _{IH} or V _{IL}	$I_{OL} = 50 \mu A$ $I_{OL} = 4 \text{ mA}$	2.0		0	0.1	_	0.1	
				3.0		0	0.1	_	0.1	
	V _{OL}			4.5		0	0.1	_	0.1	
				3.0		_	0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$	4.5			0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	t _{PLH} t _{PHL}	_	3.3 ± 0.3	15	_	7.0	11.0	1.0	13.0	ns
				50	_	9.5	14.5	1.0	16.5	
			5.0 ± 0.5	15	_	4.8	6.8	1.0	8.0	
				50	_	6.3	8.8	1.0	10.0	
Input capacitance	C _{IN}		_			4	10	_	10	pF
Power dissipation capacitance	C_{PD}			(Note 2)		18	_	_	_	pF

Note 2: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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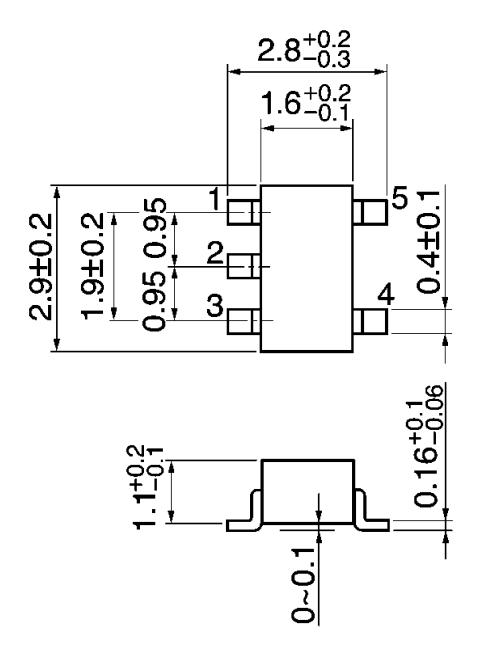
Average operating current can be obtained by the equation.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SSOP5-P-0.95

Unit: mm



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Weight: 0.016 g (typ.)

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